

Udaykiran Vangaveeti

Bachelor of Technology

ECE

Laqshya institute of technology, Telangana.

+91-9398693862



vangaveetiudaykiran9@gmail.com

[linkedin.com/in/Udaykiran Vangaveeti](https://www.linkedin.com/in/Udaykiran Vangaveeti)

EDUCATION

Degree/Certificate	Institute/Board	CGPA/Percentage	Year
B.Tech., ECE	Laqshya institute of technology, Telangana	[61.2]	[March 2019]
Senior Secondary	Resonance Junior College	[71.9]	[March 2015]
Secondary	Krishnaveni High School	[61]	[March 2013]

EXPERIENCE

- DELOPT devision of jk paper ltd.**  NOV 2022 - Till Date
Engineer Bangalore, India
 - Developed real-time embedded logic for mission-critical applications, achieving 25 percentage improvement in processing efficiency for torpedo systems.
 - Implemented optimized DMA-based data acquisition techniques, enhancing data throughput by 30 Percentage.
 - Conducted Acceptance Test Procedure on ARM-based systems to validate real-time responsiveness and control flow integrity.
 - Presented technical advancements at internal R and D review meets, receiving commendation for firmware innovation.
- Capgemini Engineering**  March 2021 - Nov 2022
Associate Engineer Bangalore, India
 - Engineered a Bootloader and peripheral driver suite for real-time embedded systems, improving startup time by 20 Percentage.
 - Developed testing framework using Embedded C and UART protocols, increasing debug efficiency by 35 Percentage.

PROJECTS

- Instrumentation and Data Acquisition System:** Nov 2022 - 2024
Tools: [Embedded C, Code Composer Studio (CCS), TMS570LC4357-EP MCU] [1]
 - A real-time embedded system in EHWT torpedo to acquire and store weapon parameters, execute run logic, and monitor weapon health.
 - Developed embedded system firmware for critical run logic and sensor acquisition
 - Implemented UART communication for remote monitoring (GUI).
 - Created bare-metal routines to support peripheral control and recovery functions.
- On-Board Computer (OBC) for ALWT:** Oct 2024
Tools: Embedded C, Embedded Cross Core Studio, SHARC Processor (ADSP-21369) [2]
 - Designed drivers for SHARC-integrated peripherals enabling real-time control
 - Integrated memory handling logic to enhance processing consistency.
 - Collaborated with cross-functional teams on debugging and performance optimization.

SKILLS

- Programming Languages:** C, C++, Assembly
- Protocols:** UART, I2C, SPI, 1553B, Parallel Communication Protocols
- Hardware:** ARM Cortex, SHARC Processor (ADSP-21369), Arduino
- Tools:** GIT, Eclipse IDE, Logic Analyzers

CERTIFICATIONS

- Embedded system training from vector India (ISO Certified)** June 2019

POSITIONS OF RESPONSIBILITY

- Engineer**, Software Design and Development AUG 2022
- Associate Engineer**, Testing and Validation